

Art Unit: 2117

Gray because it is widely known in the art, to randomly insert bits to enhance the characteristics of a data stream.

Claims 9, 15, and 16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kang et al. as applied to claims 1 and 10 above, and further in view of Krachkovsky et al (U.S. Pat. Pub. No. 2006/0174185)

With respect to Claims 9 and 16, Krachkovsky et al. discloses a parity encoder (fig. 1, #26). The parity encoder is a Low-Density Parity-Check (LDPC) code apparatus. Use of LDPC codes require low Bit Error Rates.

With respect to Claim 15, Krachkovsky et al. further discloses a Reed-Solomon encoder [0007]. A Reed-Solomon encoder is an Error Correction Code (ECC) encoder. A Reed-Solomon encoder is used to correct errors due to channel